

SHENZHEN BRILLIANT CRYSTAL TECHNOLOGIC CO.,LTD.

深 圳 市 彩 晶 科 技 有 限 公 司

The specification for the following models :

CM240128-1SLDAA-5

PROPOSED BY		APPROVED
Design	Approved	

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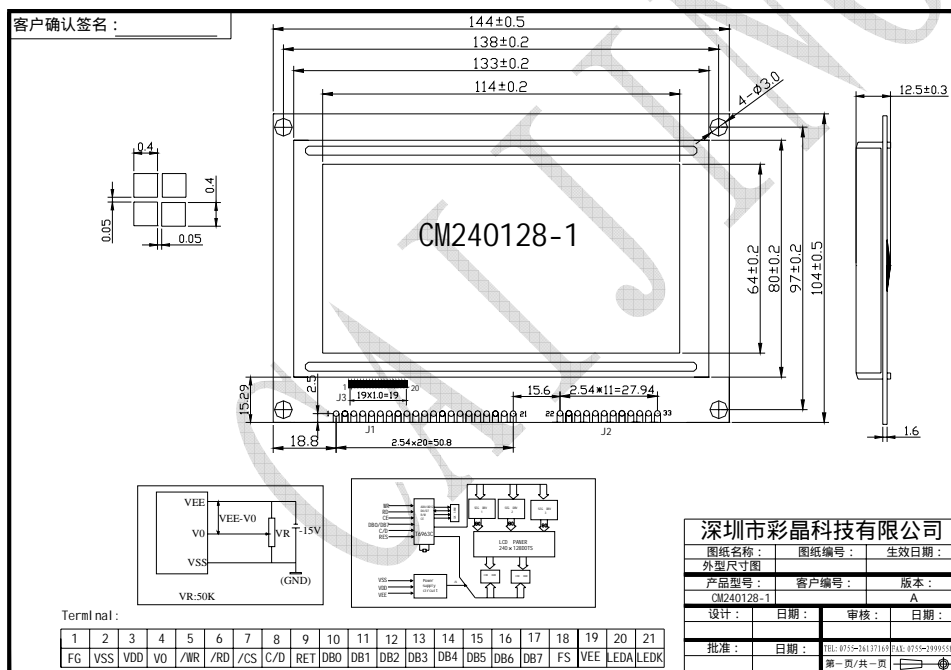
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1. Mechanical Specification

ITEM	STANDARD VALUE		UNIT
Dot Matrix	240 X 128 DOTS		--
MODULE DIMENSION	144.0 (W) X 104.0 (H) X 12.5 (T)		mm
VIEWING DISPLAY AREA	114.0 (W) X 64.0 (H)		mm
ACTIVE DISPLAY AREA	107.95 (W) X 57.55 (H)		mm
DOT SIZE	0.4 (W) X 0.4 (H)		mm
DOT PITCH	0.05 (W) X 0.05 (H)		mm
DRIVE METHOD	1/128 Duty , 1/12 BIAS		
VIEWING DIRECTION	6 O'clock		
LED Backlight Color	Yellow Green		
Backlight Input	DC +5.0V	V	mA
LED backlight current	61		mA
Backlight Half-Lift Time	35,000		HR.
INPUT VOLAGE	DC +5.0V	V	mA
LCD DRIVER	T6963C		
Weight	195g		

2. Mechanical Diagram



3. Interface Pin Connections

J1 Pin assignment :

NO	SYMBOL	LEVEL	FUNCTION
1	FG	0V	Frame ground
2	VSS	0V	Ground
3	VDD	5.0V	Supply voltage for logic
4	V0	-12V -15V	Operating voltage for LCD drive
5	/WR	L	Write signal
6	/RD	L	Read signal
7	/CE	L	Chip enable signal
8	C/D	H/L	H: Instruction code L: Data
9	/RESET	L	Reset signal
10	DB0	H/L	Data bit 0
11	DB1	H/L	Data bit 1
12	DB2	H/L	Data bit 2
13	DB3	H/L	Data bit 3
14	DB4	H/L	Data bit 4
15	DB5	H/L	Data bit 5
16	DB6	H/L	Data bit 6
17	DB7	H/L	Data bit 7
18	FS	H/L	Font select signal (H:5X8 dots; L:8X8 dots)
19	VEE	-15V	Negative Voltage for LCD driver
20	LEDA	+5V	LED Backlight
21	LEDK	0V	LED Backlight

J2 EXTERNAL INTERFACE OF MODULE :

NO	SYMBOL	LEVEL	FUNCTION
22	ED	H/L	Serial data
23	CDATA	H	Synchronus signal for row driver
24	FR	H/L	Frame signal
25	LP	H/L	Latch pulse for column driver. Shift clock pulse for row driver
26	HSCP	H/L	Shift clock pulse for column driver
27	DISP OFF/NC	---	H:Display ON.L: Display OFF / for NC
28	VDD	5.0V	Supply voltage for logic
29	VSS	0V	Ground
30	VO	-12V -15	Input voltage for LCD

31	VEE	-15V	Negative Voltage for LCD driver
32	LEDA	5V	Side light anode
33	LEDK	0V	Side light cathode

J3 EXTERNAL INTERFACE OF MODULE :

NO	SYMBOL	LEVEL	FUNCTION
1	VSS	0V	Ground
2	VDD	5.0V	Supply voltage for logic
3	VO	-12V -15V	Operating voltage for LCD drive
4	/WR	L	Write signal
5	/RD	L	Read signal
6	/CE	L	Chip enable signal
7	C/D	H/L	H: Instruction code L: Data
8	/RESET	L	Reset signal
9	DB0	H/L	Data bit 0
10	DB1	H/L	Data bit 1
11	DB2	H/L	Data bit 2
12	DB3	H/L	Data bit 3
13	DB4	H/L	Data bit 4
14	DB5	H/L	Data bit 5
15	DB6	H/L	Data bit 6
16	DB7	H/L	Data bit 7
17	FS	H/L	Font select signal (H:5X8 dots; L:8X8 dots)
18	VEE	-15V	Negative Voltage for LCD driver
19	LEDA	+5V	LED Backlight
20	LEDK	0V	LED Backlight

4. Absolute Maximum Ratings

ITEM	SYMBOL	MIN.	TYPE	MAX.	UNIT
OPERATING TEMPERATURE	TOP	0/-20	--	+50/+70	
STORAGE TEMPERATURE	TST	-10/-30	--	+60/+80	
INPUT VOLTAGE	VI	4.75	5.0	5.25	V
SUPPLY VOLTAGE FOR LOGIC	VDD-VSS	--	5.0	5.5	V
SUPPLY VOLTAGE FOR LCD	VDD-VO	--	--	17.8	V
STATIC ELECTRICITY	Be sure that you are grounded when handing LCM.				